



#18 / Declaration
T. McBeth Brown
10/16/03

PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

October 1, 2003
Date

Denise Sheridan
Denise Sheridan

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 09/153,994	Confirmation No. : 6324
Applicant : Nancey J. Hammond	Attorney Docket No.: 660082.527M (500249.01)
Filed : September 17, 1998	Customer No. : 27,076
Art Unit : 2126	
Examiner : Lewis Alexander Bullock Jr.	
Title : METHOD AND SYSTEM FOR ENHANCING RELIABILITY OF COMMUNICATION WITH ELECTRONIC MESSAGES	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

DECLARATION UNDER 37 C.F.R. § 1.131

Sir:

I, Mark W. Roberts, declare the following:

1. I am a duly appointed representative of the Assignee of the instant patent application and have access to the attorney files for the same, which includes notes and correspondence between present and prior patent counsel, the assignee, and the inventor.
2. Prior to my present employment, I was associate counsel at the firm of Seed & Berry, LLP, (prior counsel) who were original counsel for the instant application prior to transfer of the matter to the present firm. I am also familiar with the policies and practices of Seed & Berry by my prior affiliation therewith.
3. I have reviewed the notes and correspondence between prior counsel, the assignee, and the inventor. I have also reviewed the invention disclosure document No. 97.03844 that the Examiner agrees establishes conception of the present invention prior to February 19, 1998. I have also reviewed the first declaration under 37 C.F.R. § 1.131 submitted by the inventor.
4. Exhibit A is a copy of disclosure No. 97.03844 created near the date of conception of the invention, and which includes detailed descriptions of various aspects of the inventive

system and methods. Portions of Exhibit A which are not relevant to this declaration have been redacted. The actual date of conception has also been redacted, however the undersigned declares and certifies that the disclosure document was submitted by the inventor to her employer, Micron Electronics (and later assigned to the present assignee, Micron Technology, Inc.), less than one week after the date of conception

5. Exhibit B is a copy of a letter dated December 23, (about one month after the receipt of the disclosure by the employer) that was sent to prior counsel and received by the same on December 30, 1997. Non-relevant portions of the letter have been redacted. The letter shows that Micron Electronics simultaneously forwarded at least 7 invention disclosures to prior counsel at the same time, including disclosure No. 97.03844 for the instant application. Moreover, as evidenced by the titles, 4 of the 7 disclosures in this single were related to the field of managing email communications, which is the field of the present application.
6. Exhibit C is a copy of email correspondence dated January 28, 1998 between prior counsel, and Hoyt Fleming of Micron Electronics. The letter established that prior to February 19, 1998 and during the period between receipt of the several disclosures from Micron Electronics, prior counsel was diligent in preparing to prosecute the application by conducting prior art searches for the several disclosures, including disclosure No. 97.03844 .
7. Exhibit D is a copy of Email correspondence dated February 4, 1998 from prior counsel to Nancy Hammond, the inventor of the instant application, showing diligence in requesting arrangement of a face to face meeting with the inventors of the several patent disclosures, including disclosure No. 97.03844.
8. Exhibit E is a copy of Email correspondence dated February 6, 1998 from Nancy Hammond to prior counsel, showing diligence in arranging further disclosure meetings for the several applications on February 18, 2003, on which date the disclosure meeting were in fact held.
9. During the period between February 18 and September 14, 1998, prior counsel was diligent in preparing patent applications for each of the several disclosures received from Micron Electronics on December 30, 1997 that were discussed in several meetings on February 18, 2003. Exhibit F is a copy of a docket report for patent counsel, showing the filing dates of 6 of the 7 patent applications sent to patent counsel at the same time. Note that each of these were filed more or less in chronological order relative to their disclosure numbers, with the instant application being filed first.
10. Exhibit G is copy of a letter dated September 14, 1998 forwarding a completed first draft of the application from patent counsel to Nancy Hammond for review. The application was reviewed and filed on September 17, 1998.
11. During the relevant period between February 18 and September 17, 1998, original patent counsel's workload not only included preparation and filing of the aforementioned 7 patent applications for Micron Electronics, but also included work on earlier applications

for Micron Electronics, Micron Technology (together "Micron") Exhibit H is a docket report that shows that at least 155 patent applications were filed for Micron by prior counsel between the period of December 30, 1997 and September 17, 1998, the filing date of the instant application. Of these, 140 applications including the 7 sent to prior counsel on December 30, 1997, were filed during the relevant period between February 18 and September 17, 1998.

12. During the relevant period, the aforementioned 140 applications were prepared by 5 or 6 attorneys of prior counsel's firm. Accordingly, during the relevant period, on average, each attorney was preparing patent applications for Micron alone, at a rate of about 3.9 to 4.6 applications per month.
13. During the relevant period, prior counsel also worked on matters for unrelated third party clients as part of the ordinary and conventional workload of prior counsel. While docket reports pertaining to work for unrelated clients during the relevant period may be available from prior counsel, such reports are not available to the undersigned. The undersigned hereby declares and certifies, however, by knowledge derived from my former association with prior counsel, that their general policy was to work on patent applications in chronological order.
14. All of the activities described above toward conceiving of the invention, and constructive reducing the invention to practice, were conducted entirely within the United States, and more specifically, within the States of Idaho and Washington.
15. The undersigned certifies and declares that all statements made herein of my own knowledge are true, and further, that these statements were made with the knowledge that the making of willfully false statements and the like is punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and may jeopardize the validity of any patent issuing from this patent application
16. Signed this 1 day of October, 2003 at Seattle, Washington.



Mark W. Roberts, Ph.D.

INVENTION DISCLOSURE FORM

WORKING COPY

97.03844

1. INVENTOR

(a) ~~Nancey Hammond~~

2. DESCRIPTION

2.1 Title of invention

~~Automatic method of insuring that emails are received and read~~

2.2 What is the problem to be solved by your invention?

The invention provides a method of insuring that very important emails are received and read.

2.3 How did others solve the problem prior to your invention (If known)? (Describe the "prior art.") Why are these solutions non-optimal? Provide copies of any known "prior art."

Unknown

2.4 Provide a hardware block diagram of your invention. Also, describe how the hardware components of your invention are coupled together. Please include an assembly drawing if it is available. (If your invention is a pure software invention, then disregard this question.)

N/A

2.5 Provide a flow chart of the steps performed by your invention. Also, describe how your invention operates.

First embodiment of the invention:

1

Exhibit A
09/153,994

- 1) Send email with "Delivery Receipt" activated.
- 2) Determine if a "Delivery Receipt" has been received.
- 3) If, within a predetermined time a "Delivery Receipt" has not been received, then automatically resend the email.

Second embodiment of the invention:

- 1) Send email with "Read Receipt" and "Delivery Receipt" activated.
- 2) Determine if "Delivery Receipt" has been received.
- 3) Determine if "Read Receipt" has been received.
- 4) If, within a predetermined time, only "Delivery Receipt" has been received, then automatically send a second email. (This email may state that an important email has been received and not read.)

Additional embodiments may also alert the sender of the email that the send email has not been received and/or read.

2.6 Describe the advantages of your invention.

The invention provides a method of ~~insuring that very important emails are received and read~~.
No known automated methods are known.

3. CONCEPTION OF INVENTION

- 3.1 Identify the date when you first conceived the invention. (If not sure, give the earliest date of which you are sure.)
- 3.2 To whom was the idea first described and on what date? (Other than a co-inventor.)
- 3.3 Identify the date of the first tangible record such as computer simulation, tape out, drawing or written description. Please specify type and location.

This disclosure

- 3.4 Identify related invention disclosures or related patents. Attach copies, if available.

None.

3.5 IMPORTANT DATA

a. Has the invention been disclosed outside the company? No.

If yes, to whom, when, and in what form?

b. Have any articles describing your invention been published? No.

If yes, list author (s), title of article, publication and date.

c. Have any engineering samples been given out? No.

If yes, to whom and on what date?

d. Has any product using the invention been sold or offered for sale? No.

If yes, to whom and on what date?

e. Has any product that has been sold or offered for sale been manufactured or tested using the invention? No.

If yes, to whom and on what date?

3.6 When will (or did) Micron begin use of the invention experimentally?

Micron has not begun use of this invention.

3.7 When will (or did) Micron begin production of or use of this invention?

It is not know if Micron will use of this invention.

3.8 Was the invention developed during a joint development agreement or other contract with an outside company or the U.S. Government? If so, please explain.

No.

4. INVENTOR(S):

ame: Nancey Hammond

User Name: nhammond

Micron Phone: 898-4792 Micron Fax: 898-7211

Employee#: 21309 Company#: 35 Dept. #: 935

Dept Name: hpccl

Company: ☒ MEI/Nampa/PC Design and Manufacture
☐ MEI/MN/Advanced Engineering
☐ MEI/MN/ASIC Development
☐ Micron Custom Manufacturing Services
☐ SpecTek
☐ NetFrame

Home Address: 1163 W State - Eagle, ID 83616

County: Ada

izenship: U.S.

Inventor's Supervisor: Hoyt Fleming

*If more than one inventor, attach additional copies of this page, one for each inventor.

Inventor Signature: Nancey Hammond Date: _____

5. WITNESS (required for a single inventor)

If there is only one inventor, a witness should sign and date this disclosure. A witness in this case is a non-inventor who understands the nature of the invention.

Hoyt C. Fleming III
(Signature of Witness)

(Date)

(Printed Name of Witness)

Note: If you have any questions or wish assistance completing this form, please call the Legal/Patent Department, (208) 898-4790 or 8-4792.



MJP

WRITER'S DIRECT DIAL: (208) 898-4792
WRITER'S FAX: (208) 898-7211

E-MAIL: njhammond@micronpc.com

December 23, 1997

Via Federal Express (206) 622-4900

Edward W. Bulchis
Seed and Berry LLP
6300 Columbia Center
701 Fifth Avenue
Seattle, WA 98104-7092

RECEIVED

DEC 30 1997

SEED & BERRY

Re: Assignment of Micron Electronics, Inc. Disclosures

Dear Mr. Bulchis:

Enclosed you will find the following Micron Electronics, Inc. disclosure assigned to your docket for the preparation and filing of patent applications:

- MJP {
- ✓ .522 97.04041 - "Hidden Refresh In A Multibank Interleave DRAM Controller" - File
 - ✓ .523 97.04040 - "Interleaved Bank and Page Hit DRAM Controller" - File
 - ✓ .524 97.03995 - "Method of Deleting Certain Emails" - Prior Art Search on Net (Maurice Pirio)
 - ✓ .525 97.03994 - "Method of Conserving Storage Space on Electronic Messaging System" - Prior Art Search on Net (Maurice Pirio)
 - ✓ .526 97.03993 - "Method of Generating An Email" - Prior Art Search on Net (Maurice Pirio)
 - ✓ .527 97.03844 - "Automatic Method of Insuring that Emails are Received and Read" - Prior Art Search on Net (Maurice Pirio)
 - ✓ .528 97.04042 - "Method of Automatically Configuring A Cathode Ray Tube" - File

Please draft applications based on the enclosed disclosures and draft apparatus, system, and process claims in the applications where possible. These applications should be filed within 90 days of receipt. Please contact the inventors to schedule an interview. If you have any questions, please feel free to call.

Sincerely,

Nancey Hammond
Patent Secretary

/nh

Exhibit B

Enclosures

f:/legal/legal/prosecut/ltprose/assign.sb.doc

Carolyn Ross

660082.527

From: Jim White
Sent: Wednesday, January 28, 1998 3:55 PM
To: Hoyt Fleming (Micron)
Cc: Nancey Hammond (Micron); Jim White; Carolyn Ross; Maurice Piro; Victoria Sellers
Subject: status on 97.03844, 97.03993, 97.03994, & 97.03995

Hello Hoyt,

We have completed a search on the Internet for information related to each of the matters listed above. There are two documents of interest which we would like to discuss with you, particularly with respect to matters 97.03844 and 97.03994. We will send these documents to you today via Federal Express. When you have had a chance to review them, please contact us so we can discuss further work on these matters.

If you have any questions, please let us know.

Thanks,
-Jim White
(206) 622-4900

Exhibit C

Carolyn Ross

From: Jim White
Sent: Wednesday, February 04, 1998 3:44 PM
To: Nancey Hammond (Micron)
Cc: Jim White; Carolyn Ross; Maurice Pirio; Victoria Sellers
Subject: Arranging Invention Disclosure Interviews

Hello Nancey,

I would like to arrange interviews for myself and Maurice Pirio to conduct several invention disclosure meetings at your site. We are available on any of the days 2/12, 2/13, 2/17 or 2/18. If none of these dates work, please let us know and we will determine other available dates.

The matter numbers for which we would like to schedule the disclosure meetings are below. For the first six matters, we would like to schedule an initial interview. For the last matter, 97.00039/660082.465M, we would like to meet with the inventors to discuss their comments on the last draft of the application that they reviewed. If you need more information from me, please let me know.

Thanks,
-Jim White
(206) 622-4900

97.03643/660082.515M - Ann Gruell (208) 898-3739 Jon Benski (208) 898-1433
97.03722/660082.516M - Hoyt Fleming (208) 898-4790 Dave Clopton ****
97.03844/660082.527M - Nancey Hammond (208) 898-4792
97.03993/660082.526M - Dean Klein (208) 898-3110
97.03994/660082.525M - Eric Anderson (612) 604-8329
97.03995/660082.524M - Dean Klein (208) 898-3110

97.00039/660082.465M - Robert Gentile (208) 893-3650 Eric Anderson (612) 604-8329

Exhibit D

Carolyn Ross

From: Jim White
Sent: Friday, February 06, 1998 1:55 PM
To: 'CAVANWEY@micronpc.com'
Cc: Jim White; Carolyn Ross; Maurice Pirio
Subject: RE: Invention Disclosure Interviews

Christine,

The schedule looks great. Thanks for setting it up.

-Jim White
206-622-4900

From: CAVANWEY@micronpc.com[SMTP:CAVANWEY@micronpc.com]
Sent: Thursday, February 05, 1998 12:39 PM
To: Jim White
Cc: NJHAMMOND@meigate
Subject: Invention Disclosure Interviews

Jim,

Wed. the 18th will work out great. Here is the schedule:

97.03643/515M - Ann Gruell & Jon Benski	8:00AM - 9:00
97.03994/525M - Eric Anderson	9:00AM - 9:45
97.00039/465M - Eric Anderson & Robert Gentile	9:45AM - 10:30
97.03993/526M & 97.03995/524M Dean Klein	10:30AM - 12:00

****LUNCH****

97.03844/527M - Nancey Hammond	2:00PM - 3:00
97.03722/516M - Hoyt Fleming & Dave Clopton	3:00PM - 4:00

Let me know if this is going to work out okay.

Thanks
Christine Van Wey
(208) 898-4798

Exhibit E

CPI Docket #	Sub #	Client/Matter	Client and/or		Client Name	Title of Application	Filing Date
			SB Client/MT No.				
500241	0.01	446602-00777	MUEI-0082.00		Micron Technology, Inc.	Apparatus for Controlling Refresh of a Multibank Memory Device	
		361596-00282	97.04041.00	was Micron Electronics, Inc.			
			660082.522A		11/19/1998		
500243	0.01	446602-00779	MUEI-0081.00		Micron Technology, Inc.	Apparatus for Controlling a Multibank Memory Device	
		361596-00284	97.04040.00	was Micron Electronics, Inc.			
			660082.523A		12/23/1998		
500245	0.01	446602-01218	MUEI-0418.01		Micron Technology, Inc.	System for managing redundant electronic messages	
		361596-00286	97.03995.01	was Micron Electronics, Inc.			
			660082.524A		7/12/1999		
500247	0.01	446602-01220	MUEI-0417.00		Micron Technology, Inc.	Method of Conserving Storage Space On Electronic Messaging Systems	
		361596-00288	97.03994.00	was Micron Electronics, Inc.			
			660082.525M		1/15/1999		
500248	0.01	446602-01221	MUEI-0416.00		Micron Technology, Inc.	Method of generating an e-mail	
		361596-00289	97.03993.00	was Micron Electronics, Inc.			
			660082.526M		7/12/1999		
500249	0.01	446602-01246	MUEI-0415.00		Micron Technology, Inc.	Method and System for Enhancing Reliability of Communication with Electronic Messages	
		361596-00290	97.03844.00	was Micron Electronics, Inc.			
			660082.527M		9/17/1998		

Exhibit F

RAMSEY M. AL-SALAM
ROBERT J. BAYNHAM
EDWARD W. BULCHIS
DAVID V. CARLSON
CHRISTOPHER J. DALEY-WATSON
DAVID H. DEITS
MICHAEL J. DONOHUE
WILLIAM O. FERRON, JR.
KARL R. HERMANN
DAVID J. MAKI
DAVID D. McMASTERS
PAUL T. MEIKLEJOHN
MAURICE J. PIJO
GEORGE C. RONDEAU, JR.
RICHARD W. SEED
RICHARD G. SHARKEY

BENJAMIN F. BERRY
(1918-1989)

Of Counsel
ELLEN M. BIERMAN

FRANK ABRAMONTE
DALE C. BARR
TODD M. BECKER

LAW OFFICES
SEED AND BERRY LLP
6300 COLUMBIA CENTER
701 FIFTH AVENUE
SEATTLE, WASHINGTON 98104-7092
(206) 622-4900

FAX: (206) 682-6031

PATENT, TRADEMARK, COPYRIGHT, UNFAIR COMPETITION,
COMPUTER LAW, BIOTECHNOLOGY LAW, AND RELATED
LITIGATION AND LICENSING

BRIAN G. BODINE
KEVIN S. COSTANZA
THOMAS L. EWING*
FRITZ M. FLIEGEL
CLIFTON G. GREEN
JENNY A. HELLMANN
BRIAN L. JOHNSON*
ANN T. KADLECEK
STEVEN D. LAWRENZ
THOMAS E. LOOP
JAN C. L. MAXWELL*
CAROL NOTTENBURG
DAVID W. PARKER
PAUL T. PARKER
STEPHEN J. ROSENMAN
KEVIN S. ROSS
PAUL F. RUSYN
BRYAN A. SANTARELLI
GARY J. SPEIER, JR.*
JOHN C. STEWART
E. RUSSELL TARLETON
JOHN M. WECHKIN
JAMES A. D. WHITE
ROBERT G. WOOLSTON
*ADMITTED ONLY IN JURISDICTIONS
OTHER THAN WASHINGTON STATE

September 14, 1998

VIA DHL

Hoyt A. Fleming, III, Esq.
c/o Ms. Nancey Hammond
Micron Electronics, Inc.
900 East Karcher Road, Trailer No. 12
Nampa, Idaho 83687

Re: New Patent Application Entitled
METHOD AND SYSTEM FOR ENHANCING RELIABILITY
OF COMMUNICATION WITH ELECTRONIC MESSAGES
Our Reference: 660082.527M
Your Disclosure No.: 97.03844

Dear Hoyt:

Enclosed are two copies of the above-identified patent application for review by Nancey Hammond to ensure that it is complete and accurate. If you or Ms. Hammond have any changes for the description and/or drawings, please make the necessary revisions on one of the copies and return it to us for retyping. However, if the revisions are not too extensive, please ask Ms. Hammond to initial and date the revisions and return them to us along with the executed filing documents. The other copy is for your file.

If the application requires no revisions, please ask the inventor to sign and date the enclosed Declaration. The Declaration should be signed by Ms. Hammond with her full legal name. The Patent and Trademark Office regulations require that the application be executed in the form in which it is intended to be filed and that the Declaration be attached to and refer to the specification and claims of the application. Please return the signed Declaration and a copy of the application to our office.

Exhibit G

Hoyt A. Fleming, III, Esq.
September 14, 1998
Page 2

660082.527M

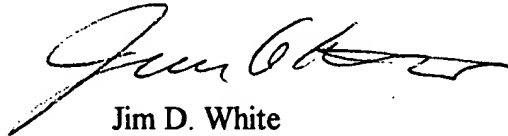
By signing the Declaration the inventor is, in effect, declaring that she does not know or believe that this invention was ever known or used in the United States, patented or described in any printed publication in any country before her invention, or more than one year prior to filing this application, or in public use or on sale in the United States more than one year prior to filing this application.

Also enclosed are an original Assignment document which must be executed before a Notary Public and an original Election Under 37 C.F.R. §§ 3.71 and 3.73 and Power of Attorney. Please return the executed Assignment and Election for filing with the Patent Office.

The Patent and Trademark Office regulations require that the inventor, and anyone associated with the inventor, such as the assignee, have a duty to disclose to the Patent and Trademark Office any information that may be important in the decision of the Patent Office Examiner in allowing the application to issue as a patent. If you are aware of any such information which you have not previously called to our attention, please let us know.

Very truly yours,

SEED and BERRY LLP



Jim D. White
Maurice J. Pirio

MJP/JDW:clr

Enclosures:

Application (2)
Declaration
Assignment
Election

CaseNum	SubCase	Attorney	Status	ApplNumber	FileDate	PatNumber	IssDate	Title
500070	1	PFR	Granted	6090	01/13/98	6038672	03/14/00	A PORTABLE COMPUTER WITH LOW POWER CD-PLAYER MODE (AS AMENDED)
500119	1	EWB	Granted	6083	01/13/98	6085329	07/04/00	METHOD FOR CONTROLLING A PERIPHERAL DEVICE IN A COMPUTER SYSTEM WITH MINIMAL CPU INTERVENTION
500653	2	EWB	Abandoned	6698	01/14/98			METHOD AND SYSTEM FOR INTERFACING A PLURALITY OF BUS REQUESTERS WITH A COMPUTER BUS
500080	1	KNE	Granted	12036	01/22/98	6138254	10/24/00	METHOD AND APPARATUS FOR REDUNDANT LOCATION ADDRESSING USING DATA COMPRESSION
500104	2	EWB	Granted	13769	01/27/98	5917762	06/29/99	CIRCUIT AND METHOD FOR PROVIDING A SUBSTANTIALLY CONSTANT TIME DELAY OVER A RANGE OF SUPPLY VOLTAGES
500217	1	EWB	Granted	15855	01/29/98	6106566	08/22/00	UPGRADABLE ELECTRONIC MODULE AND SYSTEM USING SAME
500218	1	EWB	Granted	15866	01/29/98	6044427	03/28/00	UPGRADABLE MOBILE PROCESSOR MODULE AND METHOD FOR IMPLEMENTING SAME
500515	2	EWB	Granted	16055	01/30/98	6058450	05/02/00	METHOD AND SYSTEM FOR APPORTIONING COMPUTER BUS BANDWIDTH
500335	2	SHA	Granted	18921	02/05/98	6054015	04/23/00	SUBSTRATES TO A CHEMICAL-MECHANICAL PLANARIZATION MACHINE
500369	1	KNE	Granted	20696	02/09/98	5998931	12/07/99	METHOD AND APPARATUS FOR CONTROLLING ELECTROSTATIC COUPLING TO PLASMAS
500090	1	KNE	Granted	21968	02/11/98	6130468	10/10/00	FUSE, MEMORY INCORPORATING SAME AND METHOD
500229	1	MWR	Granted	09/023389	02/13/98	6390371	05/21/02	METHOD AND SYSTEM FOR DISPLAYING INFORMATION UNIFORMLY ON TETHERED AND REMOTE INPUT DEVICES
501083	1	KNE	Granted	09/0233254	02/13/98	5936877	08/10/99	DIE ARCHITECTURE ACCOMMODATING HIGH-SPEED SEMICONDUCTOR DEVICES
500093	1	EWB	Granted	24367	02/17/98	5923594	07/13/99	METHOD AND APPARATUS FOR COUPLING DATA FROM A MEMORY DEVICE USING A SINGLE ENDED READ DATA PATH
500388	1	PFR	Granted	24826	02/17/98	6161204	12/12/00	METHOD AND APPARATUS FOR TESTING SRAM MEMORY CELLS
500061	1	PFR	Granted	27111	02/18/98	6163044	12/19/00	METHOD AND CIRCUIT FOR LOWERING STANDBY CURRENT IN AN INTEGRATED CIRCUIT
500382	1	EWB	Granted	25213	02/18/98	6179448	01/30/01	AUTOMATED LIGHT TUNER
500067	1	SHA	Granted	27411	02/20/98	6112319	08/29/00	METHOD AND SYSTEM FOR VERIFYING THE ACCURACY OF STORED DATA

500055	1	EWB	Granted	32231	02/27/98	6107157	08/22/00	METHOD AND APPARATUS FOR TRENCH ISOLATION PROCESS WITH PAD GATE AND TRENCH EDGE SPACER ELIMINATION
500103	2	SHA	Granted	09/032417	02/27/98	6194738	02/27/01	METHOD AND APPARATUS FOR STORAGE OF TEST RESULTS WITHIN AN INTEGRATED CIRCUIT
500233	1	MWR	Granted	32170	02/27/98	6044399	03/28/00	INFERRING THE IDENTITY OF A PREFERRED SERVER FROM CONFIGURATION INFORMATION
500300	1	PFR	Granted	32230	02/27/98	6004196	12/21/99	AND CLEANING OF A POLISHING PAD USED IN CHEMICAL-MECHANICAL POLISHING OF MICROELECTRONIC SUBSTRATES
500389	1	EWB	Granted	32414	02/27/98	6063700	05/16/00	METHOD OF FORMING OHMIC CONDUCTIVE COMPONENTS IN A SINGLE CHAMBER PROCESS
500411	1	EWB	Granted	09/032256	02/27/98	6269451	07/31/01	METHOD AND APPARATUS FOR ADJUSTING DATA DELAY
500417	1	KNE	Granted	32182	02/27/98	6150706	11/21/00	CAPACITOR ANTIFUSE STRUCTURE HAVING A BARRIER-LAYER ELECTRODE AND IMPROVED BARRIER LAYER
500417	2	KNE	Granted	260994	02/27/98	5969983	10/19/99	CAPACITOR ANTIFUSE STRUCTURE HAVING A BARRIER-LAYER ELECTRODE AND IMPROVED BARRIER LAYER
500419	1	SHA	Granted	32181	02/27/98	6137119	10/24/00	FROM A SUBSTRATE DURING MANUFACTURE OF AN INTEGRATED CIRCUIT AND CONNECTED TO THE SUBSTRATE AFTER MANUFACTURE
500425	1	MWR	Granted	32229	02/27/98	6100198	08/08/00	POST-PLANARIZATION, PRE-OXIDE REMOVAL OZONE TREATMENT
500235	1	SHA	Granted	33943	03/02/98	6192478	02/20/01	SECURING RESTRICTED OPERATIONS OF A COMPUTER PROGRAM USING A VISUAL KEY FEATURE
500435	1	EWB	Granted	36700	03/06/98	6212482	04/03/01	CIRCUIT AND METHOD FOR SPECIFYING PERFORMANCE PARAMETERS IN INTEGRATED CIRCUITS
500215	1	SHA	Granted	09/037361	03/09/98	6367020	04/02/02	SYSTEM FOR AUTOMATICALLY INITIATING A COMPUTER SECURITY AND/OR SCREEN SAVER MODE
500216	1	SHA	Granted	09/037360	03/09/98	6401209	06/04/02	METHOD FOR AUTOMATICALLY INITIATING A COMPUTER SECURITY AND/OR SCREEN SAVER MODE
500267	2	EWB	Granted	42129	03/12/98	5920516	07/06/99	CIRCUIT AND METHOD FOR ENABLING A FUNCTION IN A MULTIPLE MEMORY DEVICE MODULE
500347	2	PFR	Granted	41859	03/12/98	5982682	11/09/99	SELF-TEST CIRCUIT FOR MEMORY INTEGRATED CIRCUITS
500066	1	SHA	Granted	36504	03/16/98	6052800	04/18/00	METHOD AND SYSTEM FOR UPDATING INFORMATION ON AN INTELLIGENT DISPLAY DEVICE MONITORING A COMPUTER SYSTEM
500481	1	EWB	Granted	45609	03/20/98	6111446	08/29/00	INTEGRATED CIRCUIT DATA LATCH DRIVER CIRCUIT

500308	2	EWB	Granted	47759	03/24/98	5841723	11/24/98	METHOD AND APPARATUS FOR PROGRAMMING ANTI-FUSES USING AN ISOLATED WELL PROGRAMMING CIRCUIT
500356	1	EWB	Granted	47760	03/24/98	6167541	12/26/00	METHOD FOR DETECTING OR REPAIRING INTERCELL DEFECTS IN MORE THAN ONE ARRAY OF A MEMORY DEVICE
500092	1	SHA	Granted	52794	03/31/98	6078973	06/20/00	APPARATUS FOR MODEM INTERFACE IN A COMPUTER SYSTEM
500109	1	EWB	Granted	52875	03/31/98	6134609	10/17/00	METHOD FOR MODEM INTERFACE IN A COMPUTER SYSTEM
500234	1	SHA	Granted	09/052808	03/31/98	6367073	04/02/02	CENTERIALIZED AUTOMATED INSTALLATION OF SOFTWARE PRODUCTS
500392	1	SHA	Pending	09/054275	04/02/98			METHOD AND APPARATUS FOR COUPLING A SEMICONDUCTOR DIE TO DIE TERMINALS
500532	2	SHA	Granted	55811	04/06/98	6242865	06/05/01	FIELD EMISSION DISPLAY DEVICE WITH FOCUSING ELECTRODES AT THE ANODE AND METHOD FOR CONSTRUCTING SAME
500299	2	SHA	Granted	09/059793	04/13/98	5910043	06/08/99	POLISHING PAD FOR CHEMICAL-MECHANICAL PLANARIZATION OF A SEMICONDUCTOR WAFER
500043	1	PFR	Granted	60164	04/14/98	6100186	08/08/00	METHOD OF SELECTIVELY FORMING A CONTACT IN A CONTACT HOLE (AS AMENED)
500081	1	EWB	Granted	61859	04/17/98	6029252	02/22/00	SIGNALS AND CIRCUITRY, MEMORY DEVICES, AND COMPUTER SYSTEMS USING SAME
500137	3	EWB	Granted	63418	04/20/98	5898635	04/27/99	POWER-UP CIRCUIT RESPONSIVE TO SUPPLY VOLTAGE TRANSIENTS
500068	1	KNE	Granted	66035	04/24/98	6172935	01/09/01	SYNCHRONOUS DYNAMIC RANDOM ACCESS MEMORY DEVICE
500635	2	EWB	Granted	09/066526	04/24/98	5978872	11/02/99	METHOD AND SYSTEM FOR CONCURRENT COMPUTER TRANSACTION PROCESSING
500620	2	EWB	Granted	67338	04/27/98	6029223	02/22/00	ADVANCED PROGRAMMABLE INTERRUPT CONTROLLER
500086	1	EWB	Granted	09/069224	04/28/98	5978297	11/02/99	METHOD AND APPARATUS FOR STROBING ANTIFUSE CIRCUITS IN A MEMORY DEVICE
500416	1	EWB	Granted	69486	04/28/98	6178501	01/23/01	METHOD AND APPARATUS FOR INITIALIZING A MEMORY DEVICE
500502	1	MWR	Granted	67801	04/28/98	6159855	12/12/00	ORGANOMETALLIC COMPOUND MIXTURES IN CHEMICAL VAPOR DEPOSITION
500373	1	PFR	Granted	70558	04/30/98	6058056	05/02/00	DATA COMPRESSED CIRCUIT AND METHOD FOR TESTING MEMORY DEVICES

500270	2	EWB	Granted	72876	05/04/98	5903509	05/11/99	MEMORY DEVICE WITH MULTIPLE INTERNAL BANKS AND STAGGERED COMMAND EXECUTION
500020	1	SHA	Granted	75391	05/08/98	6034417	03/07/00	SEMICONDUCTOR STRUCTURE HAVING MORE USABLE SUBSTRATE AREA AND METHOD FOR FORMING SAME
500415	1	KNE	Granted	09/074952	05/08/98	6172899	01/09/01	IMPROVED STATIC-RANDOM-ACCESS-MEMORY CELL
500573	1	SHA	Granted	09/079138	05/14/98	6338663	01/15/02	LOW-VOLTAGE CATHODE FOR SCRUBBING CATHODOLUMINESCENT LAYERS FOR FIELD EMISSION DISPLAYS AND METHOD
500117	1	PFR	Granted	83830	05/22/98	6049505	04/11/00	METHOD AND APPARATUS FOR GENERATING MEMORY ADDRESSES FOR TESTING MEMORY DEVICES
500219	1	KNE	Pending	09/083959	05/22/98			METHOD AND SYSTEM FOR SELECTING COMPATIBLE PROCESSORS TO ADD TO A MULTIPROCESSOR COMPUTER
500393	1	PFR	Granted	83956	05/22/98	6163863	12/19/00	METHOD AND CIRCUIT FOR COMPRESSING TEST DATA IN A MEMORY DEVICE
500414	1	SHA	Granted	83835	05/22/98	6124140	09/26/00	METHOD AND APPARATUS FOR MEASURING FEATURES OF A SEMICONDUCTOR DEVICE
500574	1	SHA	Granted	09/085333	05/26/98	6326725	12/04/01	FOCUSING ELECTRODE FOR FIELD EMISSION DISPLAYS AND METHOD
500650	2	EWB	Granted	85533	05/27/98	6185696	02/06/01	SYSTEM FOR A PRIMARY BIOS ROM RECOVERY IN A DULA BIOS ROM COMPUTER
500478	2	EWB	Granted	86401	05/28/98	6016282	01/18/00	CLOCK VERNIER ADJUSTMENT
500409	1	SHA	Granted	87420	05/29/98	6210257	04/03/01	AND USING WEB-FORMAT POLISHING PADS IN MECHANICAL AND CHEMICAL-MECHANICAL PLANARIZATION OF MICROELECTRONIC
500213	1	EWB	Granted	09/092585	06/05/98	6311245	10/30/01	METHOD FOR TIME MULTIPLEXING A LOW-SPEED AND A HIGH-SPEED BUS OVER SHARED SIGNAL LINES OF PHYSICAL BUS
500408	1	EWB	Granted	92322	06/05/98	6023429	02/08/00	METHOD AND APPARATUS FOR GENERATING A SIGNAL WITH A VOLTAGE INSENSITIVE OR CONTROLLED DELAY
500474	1	KNE	Granted	09/092548	06/05/98	6405280	06/11/02	PLURALITY OF ORDERINGS FOR DATA BLOCK TRANSFERS WITHIN A BURST SEQUENCE
500505	1	EWB	Granted	09/092588	06/05/98	6330667	12/11/01	SYSTEM FOR READ ONLY MEMORY SHADOWING
500506	1	EWB	Granted	92460	06/05/98	6216224	04/10/01	METHOD FOR READ ONLY MEMORY SHADOWING
500658	1	EWB	Granted	09/092586	06/05/98	6425041	07/23/02	TIME-MULTIPLEXED MULTI-SPEED BUS

500044	2	EWB	Granted	09/093579	06/08/98	6347352	02/12/02	PARALLEL ARCHITECTURE COMPUTER SYSTEM AND METHOD
500276	3	EWB	Granted	94439	06/09/98	5956275	09/21/99	MEMORY-CELL ARRAY AND A METHOD FOR REPAIRING THE SAME
500237	1	SHA	Abandoned	09/095852	06/10/98			SYSTEM FOR DETECTING PHOTOCOPIED OR LASER-PRINTED DOCUMENTS
500238	1	SHA	Granted	09/095433	06/10/98	6400834	06/04/02	METHOD FOR DETECTING PHOTOCOPIED OR LASER-PRINTED DOCUMENTS
500258	1	PFR	Granted	96279	06/11/98	6178532	01/23/01	ON-CHIP CIRCUIT AND METHOD FOR TESTING MEMORY DEVICES
500430	1	SHA	Granted	09/096488	06/11/98	6324657	11/27/01	ON-CHIP TESTING CIRCUIT AND METHOD FOR IMPROVING TESTING OF INTEGRATED CIRCUITS
500332	2	MWR	Granted	97557	06/15/98	6044851	04/04/00	CLEANING COMPOSITION CONTAINING TETRAALKYLAMONIUM SALT AND USE THEREOF IN SEMICONDUCTOR FABRICATION
500413	1	KNE	Granted	103628	06/23/98	6094727	07/25/00	METHOD AND APPARATUS FOR CONTROLLING THE DATA RATE OF A CLOCKING CIRCUIT
500455	1	EWB	Granted	103763	06/24/98	6195762	02/27/01	CIRCUIT AND METHOD FOR MASKING A DORMANT MEMORY CELL
500432	1	EWB	Abandoned	104423	06/25/98			METHOD AND SYSTEM FOR PROCESSING COMMANDS IN A PACKETIZED DYNAMIC RANDOM ACCESS MEMORY
500424	1	SHA	Granted	09/106813	06/29/98	6286115	09/04/01	ON-CHIP TESTING CIRCUIT AND METHOD FOR INTEGRATED CIRCUITS
500056	2	EWB	Granted	89918	06/30/98	6154104	11/28/00	HIGH PERMEABILITY TAPPED TRANSMISSION LINE
500294	2	MWR	Granted	107353	06/30/98	5936733	08/10/99	WAFER THICKNESS IN CHEMICAL-MECHANICAL POLISHING OF SEMICONDUCTOR WAFERS
500566	1	MWR	Granted	09/109003	07/01/98	6271139	08/07/01	POLISHING SLURRY AND METHOD FOR CHEMICAL-MECHANICAL POLISHING
500656	1	KNE	Granted	108572	07/01/98	6052798	04/18/00	SYSTEM AND METHOD FOR REMAPPING DEFECTIVE MEMORY LOCATIONS
500572	1	EWB	Granted	109955	07/02/98	6190223	02/20/01	COMPOSITE SELF-ALIGNED EXTRACTION GRID AND IN-PLANE FOCUSING RING, AND METHOD OF MANUFACTURE
500427	1	EWB	Granted	113940	07/10/98	6192495	02/20/01	ON-BOARD TESTING CIRCUIT AND METHOD FOR IMPROVING TESTING OF INTEGRATED CIRCUITS
500271	2	KNE	Granted	115104	07/13/98	6121785	09/19/00	CIRCUIT AND A METHOD FOR CONFIGURING PAD CONNECTIONS IN AN INTEGRATED DEVICE

500271	3	KNE	Granted	115103	07/13/98	6133053	10/17/00	CIRCUIT AND A METHOD FOR CONFIGURING PAD CONNECTIONS IN AN INTEGRATED DEVICE
500136	3	EWB	Granted	116767	07/16/98	5949737	09/07/99	MEMORY DEVICE AND METHOD FOR READING DATA THEREFROM
500520	2	SHA	Granted	09/116828	07/16/98	6372530	04/16/02	COLD-CATHODE EMITTER AND METHOD FOR FORMING THE SAME
500520	3	SHA	Granted	09/116685	07/16/98	5977698	11/02/99	COLD-CATHODE EMITTER AND METHOD FOR FORMING THE SAME
500340	2	SHA	Abandoned	120392	07/21/98			POLISHING PADS USED IN CHEMICAL-MECHANICAL PLANARIZATION OF SEMICONDUCTOR WAFERS
500568	1	SHA	Granted	120988	07/22/98	6028322	02/22/00	DOUBLE FIELD OXIDE IN FIELD EMISSION DISPLAY AND METHOD
500639	1	EWB	Granted	09/121259	07/22/98	6363502	03/26/02	METHOD FOR MEMORY ERROR HANDLING
500441	1	MWR	Granted	122187	07/23/98	6220934	04/24/01	METHOD AND APPARATUS FOR CONTROLLING PH DURING PLANARIZATION AND CLEANING OF MICROELECTRONIC SUBSTRATES
500230	1	MWR	Pending	09/122518	07/24/98			INTEGRATED APPLICATION MANAGEMENT SYSTEM
500365	1	KNE	Granted	124927	07/29/98	5973975	10/26/99	METHOD AND CIRCUIT FOR SHARING SENSE AMPLIFIER DRIVERS
500461	1	MWR	Granted	126493	07/29/98	6190494	02/20/01	METHOD AND APPARATUS FOR ELECTRICALLY ENDPOINTING A CHEMICAL-MECHANICAL PLANARIZATION PROCESS
500569	1	SHA	Granted	09/126494	07/29/98	6278229	08/21/01	EXTRACTION GRID FOR FIELD EMISSION DISPLAYS AND METHOD
500434	1	SHA	Granted	126318	07/30/98	6173905	01/16/01	METHOD AND SYSTEM FOR BYPASSING PIPELINES IN A PIPELINED MEMORY COMMAND GENERATOR
500567	1	SHA	Granted	09/126695	07/30/98	6436788	08/20/02	FIELD EMISSION DISPLAY HAVING REDUCED OPTICAL SENSITIVITY AND METHOD
500648	2	MWR	Granted	127014	07/31/98	6033278	03/07/00	FIELD EMISSION DISPLAY WITH NON-EVAPORABLE GETTER MATERIAL
500648	3	MWR	Granted	127013	07/31/98	6127777	10/03/00	FIELD EMISSION DISPLAY WITH NON-EVAPORABLE GETTER MATERIAL
500224	1	MWR	Granted	09/128410	08/03/98	6219765	04/17/01	MEMORY PAGING CONTROL APPARATUS
500225	1	MWR	Granted	09/128403	08/03/98	6219764	04/17/01	MEMORY PAGING CONTROL METHOD

500085	1	PFR	Granted	09/130632	08/06/98	6072737	06/06/00	METHOD AND APPARATUS FOR TESTING AN EMBEDDED DRAM
500570	1	MWR	Granted	09/130634	08/06/98	6323587	11/27/01	TITANIUM SILICIDE NITRIDE EMITTERS AND METHOD
500289	2	SHA	Granted	09/132693	08/11/98	6380086	04/30/02	HIGH-SPEED PLANARIZATING APPARATUS FOR CHEMICAL MECHANICAL PLANARIZATION OF SEMICONDUCTOR WAFERS
500012	2	EWB	Granted	133919	08/13/98	6055654	04/25/00	METHOD AND APPARATUS FOR READING COMPRESSED TEST DATA FROM MEMORY DEVICES
500464	1	SHA	Granted	09/133384	08/13/98	6297877	10/02/01	METHODS FOR COMPENSATING FOR LENS HEATING RESULTING FROM WAFER REFLECTANCE IN MICRO-PHOTOLITHOGRAPHY EQUIPMENT
500022	2	SHA	Granted	134279	08/14/98	6057602	05/02/00	CHEMICAL-MECHANICAL PLANARIZATION PROCESSING OF SEMICONDUCTOR WAFERS
500281	4	MWR	Granted	137349	08/20/98	5938801	08/17/99	POLISHING PAD AND A METHOD FOR MAKING A POLISHING PAD WITH COVALENTLY BONDED PARTICLES
500563	1	EWB	Granted	09/137769	08/20/98	6359604	03/19/02	MATRIX ADDRESSABLE DISPLAY HAVING PULSE NUMBER MODULATION
500460	1	KNE	Granted	09/138861	08/24/98	6072729	06/06/00	IMPROVED DATA-OUTPUT DRIVER CIRCUIT AND METHOD
500426	1	KNE	Granted	09/139838	08/25/98	6295618	09/25/01	METHOD AND APPARATUS FOR DATA COMPRESSION IN MEMORY DEVICES
500431	1	SHA	Granted	09/139814	08/25/98	6323046	11/27/01	METHOD AND APPARATUS FOR ENDPOINTING A CHEMICAL-MECHANICAL PLANARIZATION PROCESS
500041	1	PFR	Granted	140354	08/26/98	6011727	01/04/00	BLOCK WRITE CIRCUIT AND METHOD FOR WIDE DATA PATH MEMORY DEVICES
500466	1	SHA	Pending	09/140623	08/26/98			FIELD EMISSION DISPLAY HAVING REDUCED POWER REQUIREMENTS AND METHOD
500468	1	KNE	Granted	140624	08/26/98	6125062	09/26/00	SINGLE ELECTRON MOSFET MEMORY DEVICE AND METHOD
500059	1	SHA	Granted	09/141841	08/27/98	6461774	10/08/02	APPARATUS AND METHOD FOR FORMING FEATURES ON A SUBSTRATE
500062	1	EWB	Granted	140857	08/27/98	6104209	08/15/00	LOW SKEW DIFFERENTIAL RECEIVER WITH DISABLE FEATURE
500423	1	KNE	Granted	141467	08/27/98	6167495	12/26/00	SIGNAL AND A COMMAND PACKET ERROR IN PACKETIZED DYNAMIC RANDOM ACCESS MEMORIES
500433	1	KNE	Granted	141838	08/27/98	6178488	01/23/01	METHOD AND SYSTEM FOR PROCESSING PIPELINED MEMORY COMMANDS

500469	1	KNE	Granted	141767	08/27/98	6141260	10/31/00	SINGLE ELECTRON RESISTOR MEMORY DEVICE AND METHOD
500094	1	KNE	Granted	143164	08/28/98	6111773	08/29/00	MEMORY CIRCUIT HAVING IMPROVED SENSE-AMPLIFIER BLOCK AND METHOD FOR FORMING SAME
500429	1	SHA	Granted	143526	08/28/98	6169331	01/02/01	METHOD FOR ELECTRICALLY COUPLING BOND PADS OF A MICROELECTRIC DEVICE
500445	1	PFR	Granted	09/143033	08/28/98	6338127	01/08/02	CLOCK SIGNALS USED TO LATCH RESPECTIVE DIGITAL SIGNALS, AND MEMORY DEVICE USING SAME
500095	1	SHA	Granted	09/144756	08/31/98	6352466	03/05/02	METHOD AND APPARATUS FOR WIRELESS TRANSFER OF CHEMICAL-MECHANICAL PLANARIZATION MEASUREMENTS
500438	1	EWB	Granted	145065	09/01/98	6127839	10/03/00	METHOD AND APPARATUS FOR REDUCING INDUCED SWITCHING TRANSIENTS
500571	1	SHA	Granted	09/145400	09/01/98	6439967	08/27/02	AND METHODS OF MECHANICAL AND CHEMICAL-MECHANICAL PLANARIZATION OF MICROELECTRONIC SUBSTRATE ASSEMBLIES
500011	1	SHA	Abandoned	146329	09/02/98			ELECTROSTATIC DISCHARGE PROTECTION DEVICE HAVING A GRADED JUNCTION AND METHOD FOR FORMING THE SAME
500018	1	EWB	Granted	09/146926	09/02/98	6034900	03/07/00	MEMORY DEVICE HAVING A RELATIVELY WIDE DATA BUS
500078	1	EWB	Abandoned	145852	09/02/98			MEMORY ARRAY HAVING INCREASED CELL DENSITY AND METHOD FOR FORMING THE SAME
500087	1	EWB	Granted	145866	09/02/98	6130843	10/10/00	METHOD AND CIRCUIT FOR PROVIDING A MEMORY DEVICE HAVING HIDDEN ROW ACCESS AND ROW PRECHARGE TIMES
500404	1	SHA	Granted	146055	09/02/98	6193588	02/27/01	METHOD AND APPARATUS FOR PLANARIZING AND CLEANING MICROELECTRONIC SUBSTRATES
500405	1	SHA	Granted	146056	09/02/98	6106351	08/22/00	MANUFACTURING SUCH MICROELECTRONIC SUBSTRATE ASSEMBLIES FOR USE IN MECHANICAL AND CHEMICAL-MECHANICAL
500412	1	MWR	Granted	09/146330	09/02/98	6046111	04/04/00	CHEMICAL-MECHANICAL PLANARIZATION OF MICROELECTRONIC SUBSTRATES
500428	1	KNE	Granted	145865	09/02/98	6023434	02/08/00	METHOD AND APPARATUS FOR MULTIPLE ROW ACTIVATION IN MEMORY DEVICES
500158	2	KNE	Granted	146473	09/03/98	6069504	05/30/00	ADJUSTABLE OUTPUT DRIVER CIRCUIT HAVING PARALLEL PULL-UP AND PULL-DOWN ELEMENTS
500442	1	PFR	Granted	09/146716	09/03/98	6279090	08/21/01	CLOCK SIGNALS USED IN LATCHING RESPECTIVE DIGITAL SIGNALS APPLIED TO A PACKETIZED MEMORY DEVICE
500444	1	PFR	Granted	09/146860	09/03/98	6349399	02/19/02	METHOD AND APPARATUS FOR GENERATING EXPECT DATA FROM A CAPTURED BIT PATTERN, AND MEMORY DEVICE USING SAME

500448	1	PFR	Granted	09/146946	09/03/98	6434684	08/13/02	DIFFERENT CLOCK DOMAINS, AND MEMORY DEVICE AND COMPUTER SYSTEM USING SAME
500451	1	SHA	Granted	146949	09/03/98	6191037	02/20/01	FOR FABRICATING MICROELECTRONIC COMPONENTS USING MECHANICAL AND CHEMICAL-MECHANICAL PLANARIZATION
500141	2	EWB	Granted	149707	09/08/98	6130856	10/10/00	METHOD AND APPARATUS FOR MULTIPLE LATENCY SYNCHRONOUS PIPELINED DYNAMIC RANDOM ACCESS MEMORY (AS AMENDED)
500089	1	PFR	Granted	150079	09/09/98	6029250	02/22/00	OFFSET BETWEEN A CLOCK SIGNAL AND DIGITAL SIGNALS TRANSMITTED COINCIDENT WITH THAT LOCK SIGNAL, AND MEMORY
500530	1	SHA	Granted	152772	09/10/98	6176752	01/23/01	BASEPLATE AND METHOD FOR MANUFACTURING A BASEPLATE FOR A FIELD EMISSION DISPLAY
500249	1	SHA	Pending	09/153994	09/17/98			METHOD AND SYSTEM FOR ENHANCING RELIABILITY OF COMMUNICATION WITH ELECTRONIC MESSAGE
500330	4	EWB	Granted	156098	09/17/98	5978298	11/02/99	SHARED PULL-UP AND SELECTION CIRCUITRY FOR PROGRAMMABLE CELLS SUCH AS ANTIFUSE CELLS
500509	1	SHA	Granted	156182	09/17/98	6212592	04/03/01	COMPUTER SYSTEM FOR PROCESSING SYSTEM MANAGEMENT INTERRUPT REQUESTS
500624	1	EWB	Granted	153992	09/17/98	6145048	11/07/00	METHOD OF PROCESSING SYSTEM MANAGEMENT INTERRUPT REQUESTS
500651	1	EWB	Granted	158169	09/21/98	6219755	04/17/01	UPGRADEABLE CACHE CIRCUIT USING HIGH SPEED MULTIPLEXER
500652	2	EWB	Granted	158179	09/21/98	6119197	09/12/00	METHOD FOR PROVIDING AND OPERATING UPGRADEABLE CACHE CIRCUITRY